

IN THE DRAWINGS

The attached sheets of drawings include changes to Figs. 1, 2A, 2B, 2C, 3 and 4. These sheets, which include Figs. 1, 2A, 2B, 2C, 3 and 4, replace the original sheets including Figs. 1, 2A, 2B, 2C, 3 and 4.

Attachment: Four Replacement Sheets

REMARKS/ARGUMENTS

Favorable reconsideration of this application, as presently amended and in light of the following discussion, is respectfully requested.

Claims 1-12 are pending in the present application. Claims 1-12 are amended by the present amendment at least to better comply with standard U.S. claim practice.

*to address
cosmetic
matters
of
form*

Claim amendments find support in the application as originally filed, thus, no new matter is added.

In the outstanding Office Action, the drawings were objected to as including informalities; the specification was objected to as including informalities; Claims 5 and 11 were rejected under 35 U.S.C. §112, first paragraph; and Claims 1-4, 6-10 and 12 were allowed.

In regard to the objection to the drawings, the drawings have been amended to overcome the objection. Specifically, Figures 1, 2A, 2B, 2C, 3 and 4 have been amended to include the designation "Related Art". Accordingly, Applicants respectfully request that the objection to the drawings be withdrawn.

In regard to the objection to the specification, the specification has been amended to overcome the objection. Specifically, the paragraph beginning at page 11, line 22, and the paragraph beginning at page 12, line 23 have been amended to correct the informalities as suggested in the outstanding Office Action. Accordingly, Applicants respectfully request that the objection to the specification be withdrawn.

Turning now to the §112, first paragraph, rejection of Claims 5 and 11. In response to the rejection of Claims 5 and 11, Claims 5 and 11 have been amended to recite, for example, "...pair of transistors are directly coupled to said own current source and a collector of one of the pair of low gain transistors in said low noise amplifying circuit and selectively connected

to said own current source during high gain or a collector of one of the pair of low gain transistors in said low noise amplifying circuit during low gain.” (emphasis added).

In other words, Claims 5 and 11 now recite that the emitters of the differential pairs of transistors are directly coupled to said *own current source* and *a collector* of the other of the pair of low gain transistors in said low noise amplifying circuit. Further, Claims 5 and 11 recite that the emitters of the differential pairs of transistors are selectively connected to said *own current source* during high gain or a collector of one of the pair of transistors in said low noise amplifying circuit during low gain. Thus, Claims 5 and 11 recite that the emitters are directly coupled to both the own current source and the collector, while the emitters are selectively connected to the own current source during high gain or the collector during low gain. Therefore, when terminal B2 is turned off as is described in lines 15-19 on page 17 of the current specification and no DC current passes through the claims still describe this situation as in that case the differential pairs of transistors are selectively connected to the collector.

Accordingly, Applicants respectfully request that the rejection of Claims 5 and 11 be withdrawn.

Consequently, in view of the present amendment and in light of the above discussion, the outstanding grounds for rejection are believed to have been overcome. The application as amended herewith is believed to be in condition for formal allowance. An early and favorable action to that effect is respectfully requested.

Respectfully submitted,

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